

REMARKS

This application has been carefully reviewed in light of the Office Action mailed September 8, 2005. Claims 1-24 are pending in the application. Claims 1-24 are rejected in the application. Applicant respectfully requests reconsideration and favorable action of all pending claims in view of the following remarks.

Rejections Under 35 U.S.C. §102 and §103

The Office Action rejects Claim(s) 1 [2, 6-8, 10, 12, 13, 15, 16, 20, 23, and 24] under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 4,693,770 to Hatada ("*Hatada*"), Claims 3 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Hatada*, in view of U.S. Patent No. 6,100,593 to Yu et al. ("*Yu*"), Claims 4, 5, 18 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Hatada* and *Yu*, in view of U.S. PG Publication No. 2001/0011766 to Nishizawa et al. ("*Nishizawa*"), Claims 9 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Hatada*, Claims 11 and 22 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Hatada* in view of U.S. Patent No. 6,576,998 to Hoffman ("*Hoffman*"), and Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over *Hatada* in view of U.S. PG Publication No. 2002/0072147 to Sayagani et al. ("*Sayagani*"). Applicant respectfully traverses for the reasons described below.

Claim 1, as amended, recites "a circuit having first, second and third portions, said first circuit including at least one semiconductor component and said third circuit portion including a single semiconductor circuit component" and "said second substrate having a semiconductor portion which has said single semiconductor circuit component of said third circuit portion therein and said second substrate being devoid of any other semiconductor circuit component." This combination of references is not shown by the cited references. In particular, *Hatada* does not disclose the claimed second substrate being devoid of any semiconductor component other than the single semiconductor component of the third circuit portion (nor do any of the other cited references). Applicant notes that, even if the statement made at page 4 of the Office Action that an LED is a single circuit component is correct, *Hatada* still does not disclose that element 16 of Figure 2 is a substrate having a single semiconductor component and being devoid of any other semiconductor component. This is

apparent also because the Figures of *Hatada* show numerous connections between elements 16 and 22. In addition, Claim 1 as amended, further recites "wherein the second substrate and first substrate have no electrical connection therebetween other than any electrical connection formed between the first and second electrically conductive parts of the second circuit portion and the third and fourth electrically conductive parts of the third circuit portion." This limitation is not shown in *Hatada*. Rather, *Hatada* shows electrical connections between elements 22 and 16 in excess of what is alleged to be the first and second connections of the second circuit and the third and fourth connections of the third circuit.

For at least these reasons, Claim 1 is allowable, as are all claims depending therefrom. Claim 15 is allowable for analogous reasons, as are all claims depending therefrom. Reconsideration and favorable action is requested.

New Claim 25 is also allowable at least because *Hatada* does not show "said second circuit portion including at least one passive component selected from the group consisting of a resistor, a capacitor, and an inductor." Clearly, elements 22 of *Hatada* do not meet this limitation. In addition, *Hatada* also does not show the combination of "an insulating layer overlying the first circuit portion" and "at least one via disposed within the insulating layer and electrically coupling the first circuit portion to the second portion." Clearly, neither the claimed insulating layer or the associated vias are disclosed in *Hatada*. Favorable action is requested.

Conclusion

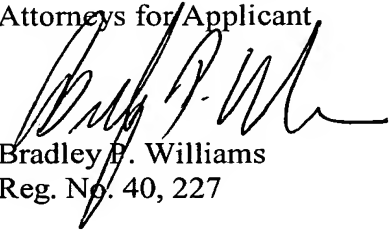
Applicant has made an earnest attempt to place this case in condition for allowance. For at least the foregoing reasons, Applicant respectfully requests full allowance of all pending claims.

If the Examiner believes that a telephone conference would advance prosecution of this Application in any manner, the Examiner is invited to contact the undersigned Attorney for Applicant at the Examiner's convenience.

Applicant believes that no fee is due. However, the Commissioner is hereby authorized to charge any additional fees or credit any overpayments to Deposit Account No. 02-0384 of Baker Botts L.L.P.

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